

Refka Ghodhbani, Ph.D.

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Personal Informations

| | |
|----------------|-----------------------|
| Nationality | 📌 Tunisian . |
| Birthday | 📌 07-07-1987 . |
| Marital Status | 📌 Married |
| CIN Number | 📌 09075867 |
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Employment History

- 2015 – 2020 📌 **Lecturer in Computer Engineering .** .Faculty of Computing And Information technology,Northers Borders University,KSA
- 2014 – 2015 📌 **Lecturer in Electronic.** .High Institute of Informatic and mathematics (ISIMM) Monastir University Tunisia

Education

- 2015 – 2021 📌 **Ph.D., Monastir University** Microelectronic.
Thesis title: *Proposal of a methodology for rapid prototyping of an automatic video surveillance system for Megapixel cameras.*
- 2010 – 2014 📌 **M.Sc. Microelectronic, Monastir University** in Microelectronic.
Master title: *Contribution to the Hardware implementation of the arithmetic encoder EBCOT for Digital Cinema.*
- 2006 – 2010 📌 **Bachelor. electronic, Monastir University** in electronic.



Courses Teaching

- 2015 – 2021 📌 **Digital Logic Design CPCS211** for Bachelor in Computers Sciences.
- 📌 **Computer Architecture CPCS214** for Bachelor in Computers Sciences.
- 📌 **Multimedia Technology CPCS482** for Bachelor in Computers Sciences.
- 📌 **Computers Graphics CPCS391** for Bachelor in Computer Sciences.
- 2014 – 2015 📌 **Electronics Laboratory** for Bachelor in Electronics.

Committees

- 2015 – 2017 📌 **Member in Registration Committee**
- 2016 – 2017 📌 **Member in Exams Committee**
- 2016 – 2018 📌 **Member in Students Activities Committee**

Committees (continued)

- 2018 – 2019  **Member in Books Committee**
- 2018 – 2021  **Member in Quality and Academic Accreditation Committee**



Research Publications:Papers

1. **Refka Ghodhbani**, Taoufik Saidani ,Layla Horrigue,and Mohamed Atri“An efficient pass-parallel architecture for embedded block coder in JPEG 2000”*Journal of Real Time Image Processing Springer* 2017 (IF=1.56)
2. **Refka Ghodhbani** ,Taoufik Saidani, Layla horrigue, Mohamed Atri, Analysis and VLSI High Speed of Parallel Causal EBCOT Architecture for Image Compression. *WSEAS TRANSACTIONS on SIGNAL PROCESSING (ISSN: 2224-3488) Vol. 17, (2017). Indexed: Scopus.*
3. **Refka Ghodhbani** ,Taoufik Saidani, Layla horrigue, Mohamed Atri,. Fast FPGA Prototyping based Real-Time Image and Video Processing with High-Level Synthesis. *International Journal of Advanced Computer Science and Applications (IJACSA) Volume 11 Issue 2, 2020.*
4. Taoufik Saidani,**Refka Ghodhbani**, Hardware Acceleration of Video Edge Detection with Hight Level Synthesis on Xilinx Zynq Platform, *Engineering, Technology and Applied Science Research (ETASR) Vol. 11, No. 6: December, 2021.*
5. Layla Horrigue, Taoufik Saidani,**Refka Ghodhbani**,Julien Dubois, Johel Miteran,MohamedAtri “An efficient hardware implementation of MQ_decoder of the JPEG2000 ” *Journal of Microprocessor and Microsystem (Elsevier) (Impact Factor=1.045).*
6. Layla horrigue, **Refka Ghodhbani**, Taoufik saidani, Mohamed Atri. GPU Acceleration of Image Processing Algorithm Based on Matlab CUDA. *International Journal of Computer Science and Network Security (IJCSNS) VOL.18 No.6, June 2018.*




Research Publications:Conerences

1. **Refka Ghodhbani**,Taoufik Saidani,Layla Horrigue,and Mohamed Atri“ Analysis and Implementation of Parallel Causal Bit Plane Coding In JPEG2000 Standard” *International Conference on Control and Information Technology ICCIT October 2014 Sousse Tunisia.*
2. **Refka Ghodhbani**, Taoufik Saidani, Layla Horrigue and Mohamed Atri “Area efficient, High Speed VLSI Design for BPC coder in JPEG 2000” *International Image Processing, Applications and Systems conference 2014 Hammamet Tunisia.*
3. Layla Horrigue, Taoufik Saidani, **Refka Ghodhbani** and Mohamed Atri “An efficient, High Speed VLSI Architecture for JPEG 2000 MQ_coder” *International Image Processing, Applications and Systems conference 2014 Hammamet Tunisia.*
4. Layla Horrigue,Taoufik Saidani, **Refka Ghodhbani**,and Mohamed Atri“ Ahigh Performance MQ Decoder Architecture in JPEG2000” *International Conference on Control and Information Technology ICCIT October 2014 Sousse Tunisia.*

Skills

- Languages  Strong reading, writing and speaking competencies for English, French, Arabic.
- Coding  MATLAB, VHDL, Assembly language , \LaTeX , ...

Skills (continued)










- Embedded System  FPGA, PostgreHDL CODER, SIMULINK, MODELSIM, VIVADO.
- Computer Skills  OFFICE WORD, Office EXCEL, Office EXCEL, Tomcat Web Server.
- Misc.  Academic research, teaching, training, consultation, ABET Accreditation, E-Learning, Blackboard, Lectora \LaTeX typesetting and publishing.

Miscellaneous Experience




Awards and Achievements

- 2018  **ABET Participation.** Awarded by NBU university.

Workshops Training

- 2020  **Courses specifications** organized by Northern borders university.
- 2019  **NCAAA Accreditation.** organized by Northern borders university.
-  **Research Publishing.** organized by Northern borders university.
- 2017  **ABET Accreditation.** organized by Northern borders university.
- 2016  **Blackboard Exams.** organized by Northern borders university.
- 2015  **Academic advising.** organized by Northern borders university.
- 2016  **Lectora Environment.** organized by Northern borders university.
- 2015  **E-Learning :Blackboard.** organized by Northern borders university.
-  **Academic advising** organized by Northern borders university..

Project Advising

- 2018  **Breast cancer detection with MATLAB** Computers Sciences graduate students.
- 2017  **Image Encryption With MATLAB** Computers Sciences graduate students.
- 2016  **Iris recognition System Using MATLAB** .Computers Sciences graduate students.

References

Mahamed ATRI Professor in Microelectronic Faculty of Sciences Monastir University Tunisia